Docket No.

201163US2S

IN RE APPLICATION OF: Junichi ASADA

SERIAL NO: 09/740,902

RCE FILED: August 9, 2002

FOR:

SEMICONDUCTOR DEVICE

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

Transmitted herewith is an amendment in the above-identified application.

- ☐ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith: Marked-up Copy; Request for Extension of Time (1 month)

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS		RATE		CALCULATIONS
TOTAL	2	MINUS	27	0	х	\$18	=	\$0.00
INDEPENDENT	2	MINUS	3	0	x	\$84	-	\$0.00
		☐ MULTIPLE DEPENDENT CLAIMS + \$280 =					\$0.00	
,		TOTAL OF ABOVE CALCULATIONS					\$0.00	
		☐ Reduction by 50% for filing by Small Entity					\$0.00	
		☐ Recordation of Assignment + \$40				=	\$0.00	
	·					тот	ΓAL	\$0.00

- A check in the amount of \$110.00 is attached.
- Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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FED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

JUNICHI ASADA : EXAMINER: CHU, CHRIS C.

SERIAL NO.: 09/740,902

RCE FILED: AUGUST 9, 2002 : GROUP ART UNIT: 2815

FOR: SEMICONDUCTOR DEVICE

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

'ANT COMMISSIONER FOR PATENTS
INGTON, D.C. 20231

In response to the Office Action dated September 30, 2002, please amend this CENTER 2800 application as follows:

IN THE CLAIMS

Please amend Claim 28 as shown in clean form below.¹

28. (Amended) A semiconductor memory device comprising:

a semiconductor element;

a plurality of lead wires connected to a plurality of connecting electrodes formed on said semiconductor element;

at least a single dummy lead wire that is not electrically connected to said semiconductor element and does not include an outer lead portion for electrically connecting said semiconductor element to an external circuit of said semiconductor element;

¹A marked-up copy of the amended claim is attached.